

REMARKS

Claims 1-8, 11-15, 20-24, 26 and 30 are pending in this application. By this Amendment, claims 1, 5, 11, 13, 22-24 and 27 are amended. Support for the amendments may be found, for example, in paragraph [0122] of the specification. No new matter is added. Applicants respectfully request reconsideration and prompt allowance of the pending claims at least in light of the following remarks.

Claims 1-8, 11, 12, 20-24 and 30 are rejected under 35 U.S.C. §103(a) as being unpatentable over Ohtaka et al. (U.S. 6,151,000) ("Ohtaka") in view of Kondo (U.S. 6,777,888). Applicants respectfully traverse the rejection.

Ohtaka and Kondo do not support the rejection for at least the reason that they fail to disclose or suggest "a plurality of pixel circuits to drive the plurality of electro-optical elements, each of the plurality of pixel circuits having a first transistor and a storage capacitor to store a data signal supplied via a data line among the plurality of data lines and the first transistor, and a reset transistor to reset the data signal stored in the storage capacitor based on a reset signal supplied via an exclusive scanning line formed with respect to the respective scanning lines," as recited in independent claim 1. Independent claims 7, 11 and 22-24 include similar recitations.

The Office Action correctly recognizes that Ohtaka is deficient as to the claimed reset transistor, but alleges that Kondo cures the deficiency by Kondo's disclosure in FIG. 3 (specifically, TFT M4 shown in FIG. 3) and col. 9, lines 16-23. However, there is no disclosure or suggestion in the cited portion or elsewhere in Kondo that TFT M4 is "to reset the data signal stored in the storage capacitor based on a reset signal supplied via an exclusive scanning line formed with respect to the respective scanning lines," as recited in independent claim 1.

Accordingly, independent claims 1, 5, 7, 11 and 22-24 are allowable over Ohtaka and Kondo. Moreover, claims 2-4, 6, 8, 12, 20 and 21 each depends on one of independent claims 1, 7, 11 and 22-24, and are therefore likewise allowable over Ohtaka and Kondo for at least that reason, as well as for the additional features they recite. Withdrawal of the rejection of claims 1-8, 11, 12, 20-24 and 30 as being unpatentable over Ohtaka in view of Kondo is therefore respectfully requested.

Claims 13-15 and 26 are rejected under 35 U.S.C. §103(a) as being unpatentable over Ohtaka in view of Kondo, and further in view of Wakitani et al. (U.S. 5,940,142) ("Wakitani"). Applicants respectfully traverse the rejection.

Independent claim 13 recites "a plurality of pixel circuits to drive the plurality of electro-optical elements, each of the plurality of pixel circuits having a first transistor and a storage capacitor to store a data signal supplied via a data line among the plurality of data lines and the first transistor, and a reset transistor to reset the data signal stored in the storage capacitor based on a reset signal supplied via an exclusive scanning line formed with respect to the respective scanning lines," and independent claim 26 includes a similar recitation.

Accordingly, claims 13 and 26 are allowable over Ohtaka and Kondo for at least the reasons discussed above. Wakitani does not cure the deficiencies in Ohtaka and Kondo, and therefore claims 13 and 26 are further allowable over the combination of Ohtaka, Kondo and Wakitani. Moreover, claims 14 and 15 depend on claim 13 and are therefore likewise allowable over the applied combination for at least that reason, as well as for the additional features they recite. Withdrawal of the rejection of claims 13-15 and 26 as being unpatentable over Ohtaka, Kondo and Wakitani is therefore respectfully requested.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of the claims are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



James A. Oliff
Registration No. 27,075

William E. Curry
Registration No. 43,572

JAO:WEC/hms

Attachment:

Request for Continued Examination

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OLIFF & BERRIDGE, PLC
P.O. Box 320850
Alexandria, Virginia 22320-4850
Telephone: (703) 836-6400

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